
List of tables

Table 1	
Releases that are currently supported on systems	2
Table 2	
Relative power of Meridian 1 CPUs	12
Table 3	
Intelligent peripheral equipment	20
Table 4	
Physical characteristics of cards and modules in Meridian 1	44
Table 5	
I/O interface for applications	48
Table 6	
Power consumption—IPE cards	51
Table 7	
Power consumption—PE cards	52
Table 8	
Meridian 1 module power consumption	53
Table 9	
Trunk traffic—Poisson 1 percent blocking	61
Table 10	
Digitone receiver load capacity—6 to 15 second holding time	67
Table 11	
I/O interface for applications	74

Table 12	
Real Time Remaining Required for Upgrade (North American stream)	77
Table 13	
Real Time Remaining Required for Upgrade (International stream) .	78
Table 14	
Real time capacity (EBC) by release and system option (EPE equipment)	80
Table 15	
Default traffic parameter values	83
Table 16	
Memory sizes (MB)	90
Table 17	
Memory card configurations (ST, STE, NT, XT, option 21, option 51, option 71)	91
Table 18	
Memory configuration by X11 release (ST, STE, NT, XT, option 21, option 51, option 71)	91
Table 19	
Memory card configurations (option 11C/51C/61C/81/81C)	92
Table 20	
DRAM size as a function of Mobility supported in Release 22 and 23	93
Table 21	
MEMAVAIL USED upper bounds for UPGRADE (units = SL-1 words)	96
Table 22	
Recommended Maximum Call Register Counts and Release 23 Memory Implications	98
Table 23	
Memory partitioning for NT, XT, 61, 71 systems	99
Table 24	
Release 23 memory map for systems equipped with NT6D66 CP cards w/ 48 MB DRAM	100

Table 25	
Release 23 DRAM map for systems equipped with NT9D19 and NT5D10 CP cards (32MB 1500 mobility users example)	101
Table 26	
Release 23 Flash EPROM map for systems with NT9D19 and NT5D10 CP cards	102
Table 27	
Program store size (kB) for NT, XT, 61, 71 assuming all packages loaded	103
Table 28	
Program store size (MB) for option 11C/51C/61C/81/81C	103
Table 30 Patching, Overhead and OS Heap (MB) for option 11C . . .	104
Table 29	
Patching, Overhead and OS Heap (MB) for option 51C/61C/81/81C w/ NT6D66 CP card	104
Table 31	
Patching and OS Heap (MB) for option 51C/61C/81/81C w/ NT9D19 card and NT5D10 card	105
Table 32	
Hard disk/floppy disk/CPU interactions	116
Table 33	
History of floppy drive upgrades	117
Table 34	
Storage space for floppy disk types	118
Table 35	
Disk space required for software in records	118
Table 36	
Data block growth by upgrade to X11 R17	119
Table 37	
Disk space calculations—ST and option 21	120
Table 38	
Floppy disk drives	121

Table 39	
Number of Bytes per SL-1 word	124
Table 40	
Program store disk capacity requirements (1 kB records)	125
Table 41	
Word to byte conversion factors	126
Table 42	
Database sizes (1 kB records)	126
Table 43	
Typically maximum number of call registers by machine type	127
Table 44	
Number of words per call register by release	127
Table 45	
Floppy Disk Space Requirements Projection for SL-1 Customer Data (MB)	129
Table 46	
Option 51C/61C/81/81C hard disk partitioning—Protected	130
Table 47	
Option 51C/61C/81/81C hard disk partitioning—Unprotected	131
Table 48	
History of hard disk sizes	132
Table 49	
Number of program floppy disks (4MB)	133
Table 50	
Floppy disk layout: 2850 records available	133
Table 51	
Mass storage media for the auxiliary processors	134
Table 52	
Average number of attempts for the number of T1 links in the trunk group	137
Table 53	
Average total RVQ attempts	139

Table 54	
RVQ parameters	140
Table 55	
Meridian Mail channel capacity	143
Table 56	
Data Link capacity for typical ACD/AML applications	145
Table 57	
MFR table with 6-second holding time	151
Table 58	
Worksheet for network loop calculation (example)	153
Table 59	
Worksheet for option 21 * card slot calculation (example)	155
Table 60	
Steady-state requirements and capacities per D-channel (outgoing and incoming)	177
Table 61	
Peak requirements per D-channel (outgoing)	178
Table 62	
ISL link capacities	180
Table 63	
Steady-state requirements and capacities per D-channel with staggered timers (outgoing and incoming)	182
Table 64	
Steady-state requirements and capacities per D-channel with immediate broadcast of Logical Call Requests (outgoing and incoming)	183
Table 65	
Peak requirements for NACD messages (outgoing)	184
Table 66	
Peak requirements for NACD messages (incoming)	185
Table 67	
Steady-state requirements and capacities per AML (outgoing and incoming)	189

Table 68	
Peak capacities for CCR statistics messages per AML (outgoing) . . .	190
Table 69	
Recommended AML flow control values	191
Table 70	
Link capacities for CDR application (outgoing)	193
Table 71	
Steady state requirements for CDR application (outgoing)	194
Table 72	
Peak requirements for <i>FCDR=new</i> (outgoing)	194
Table 73	
Capacity of MAX Module in simple calls	195
Table 74	
Steady-state requirements for Meridian MAX (outgoing)	196
Table 75	
Peak requirements for MAX (outgoing)	197
Table 76	
MSDL Engineering Worksheet	198
Table 77	
MSDL real time requirements for D-channel applications	199
Table 78	
MSDL peak buffer requirements for D-channel applications	200
Table 79	
MSDL real time requirements for AML applications	201
Table 80	
MSDL peak buffer requirements for AML applications	202
Table 81	
MSDL real time requirements for SDI applications	203
Table 82	
MSDL peak buffer requirements for SDI applications	204
Table 83	
NACD Message Model	206

Table 84	
MSDL real time requirements for D-channel applications	206
Table 85	
MSDL real time requirements for SDI applications	207
Table 86	
MSDL Engineering Worksheet	207
Table 87	
MSDL real time requirements	208
Table 88	
MSDL Engineering Worksheet	209
Table 89	
Maximum CLASS sets per group (based on inter-group junctor capacity limitation	216
Table 90	
CMOD Unit Capacity Table (P.001)	218
Table 91	
Worksheet for option 11 Card Slot Calculation	233
Table 92	
Worksheet for option 11 Card Slot Calculation (Example)	234
Table 93	
Worksheet for option 21* card slot calculation	235
Table 94	
Worksheet for option 61 card slot calculation	236
Table 95	
System power consumption worksheet	237
Table 96	
Processor load worksheet	238
Table 97	
Real time factors by release	243
Table 98	
Meridian 1 real time capacity by release (EBC)	247

Table 99	
PDS calculation worksheet	248
Table 100	
UDS calculation worksheet	253
Table 101	
PDS factors by X11 release—units are SL-1 words	258
Table 102	
UDS factors by X11 release—units are SL-1 words	262
Table 103	
Default buffer sizes for various Meridian 1 options	265
Table 104	
Memory used—ST/NT/XT/option 21/21E/51/61/71	267
Table 106	
Memory used—option 81/61C/51C w/ NT6D66 and NT9D19 CP cards and Option 11C* (Note 6)	268
Table 105	
Packing factor—ST/NT/XT/option 21/21E/51/61/71	269
Table 107	
Size of protected line block for PBX sets—units are SL-1 words	270
Table 108	
Data space requirements for PBX set features—units are SL-1 words	271
Table 109	
Size of protected line block for SL-1 sets—units are SL-1 words . . .	272
Table 110	
Data space requirements for SL-1 set features—units are SL-1 words	274
Table 111	
ST/Option 21 disk space in 1kB records (2 disks)	279
Table 112	
NT/XT/Options 51/61/71 disk space in 1kB records (2 disks)	280
Table 113	
Options 51/61/71/21E disk space in 1Kb records (2 or more disks) . .	280

Table 114	
Floppy disk count on Thor systems (Options 51C/61C/81/81C)	281
Table 115	
Mass storage space available on auxiliary processors	282
Table 116	
System parameters	283
Table 117	
Customer parameters	284
Table 118	
Console/telephone related parameters	285
Table 119	
Trunk and network related parameters	286
Table 120	
ACD feature parameters	287
Table 121	
Non-ACD feature parameters	288
Table 122	
Physical capacity/hardware related parameters	290
Table 123	
Memory related parameters	291

